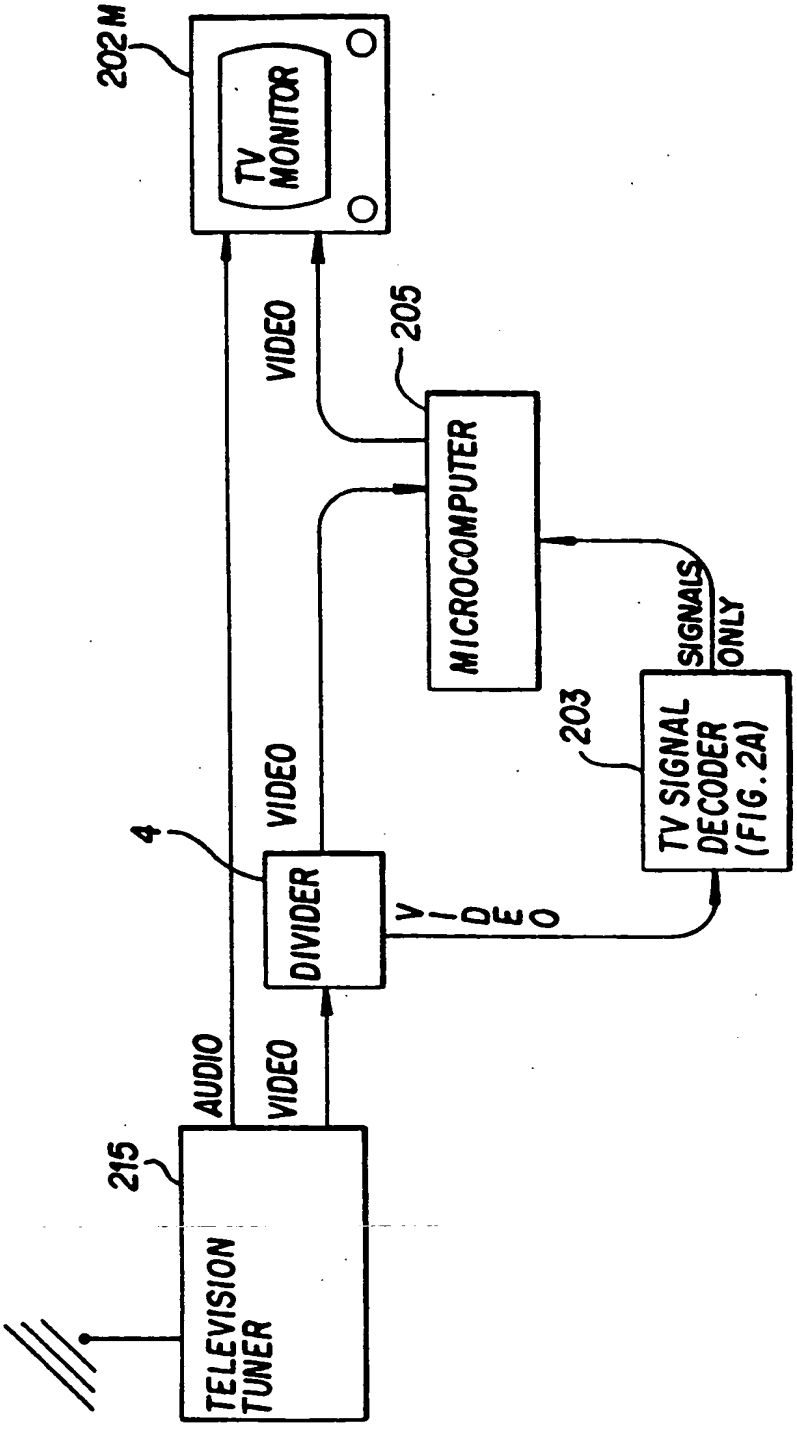

202M
TV MONITOR
VIDEO
205
MICROCOMPUTER
SIGNALS ONLY
203
TV SIGNAL DECODER (FIG. 2A)
4
VIDEO
DIVIDER
VIDEO
AUDIO
215
TELEVISION TUNER

FIG. 1

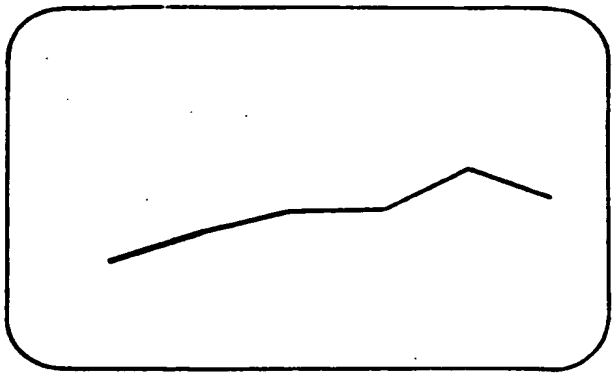


FIG. 1A

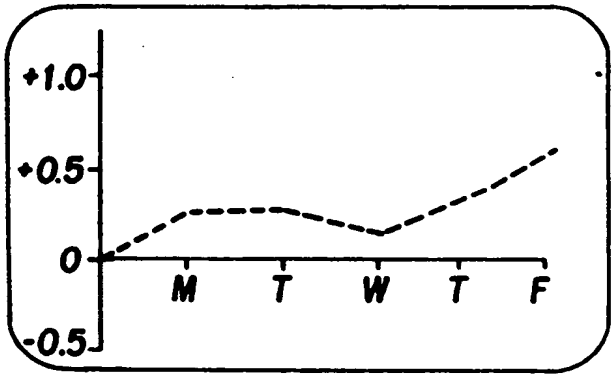


FIG. 1B

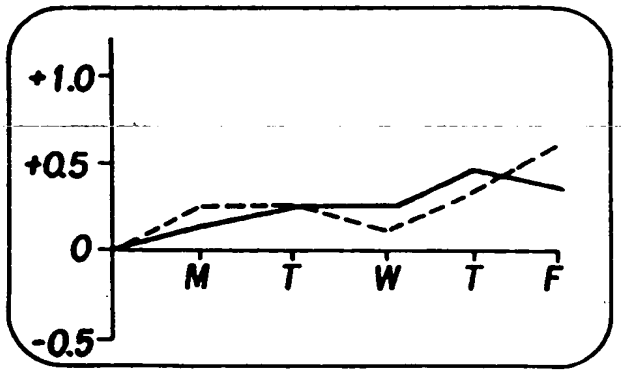


FIG. 1C

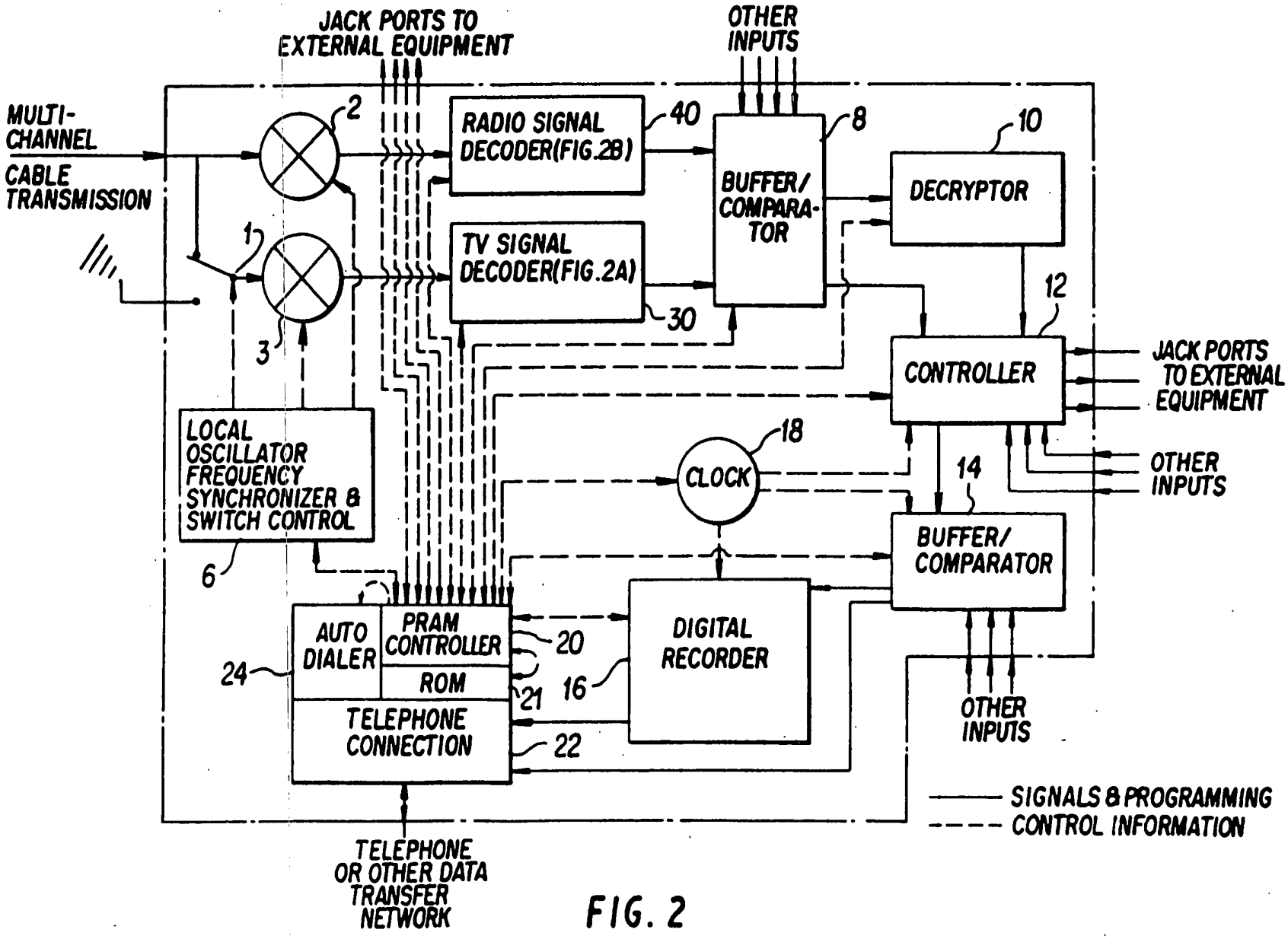


FIG. 2

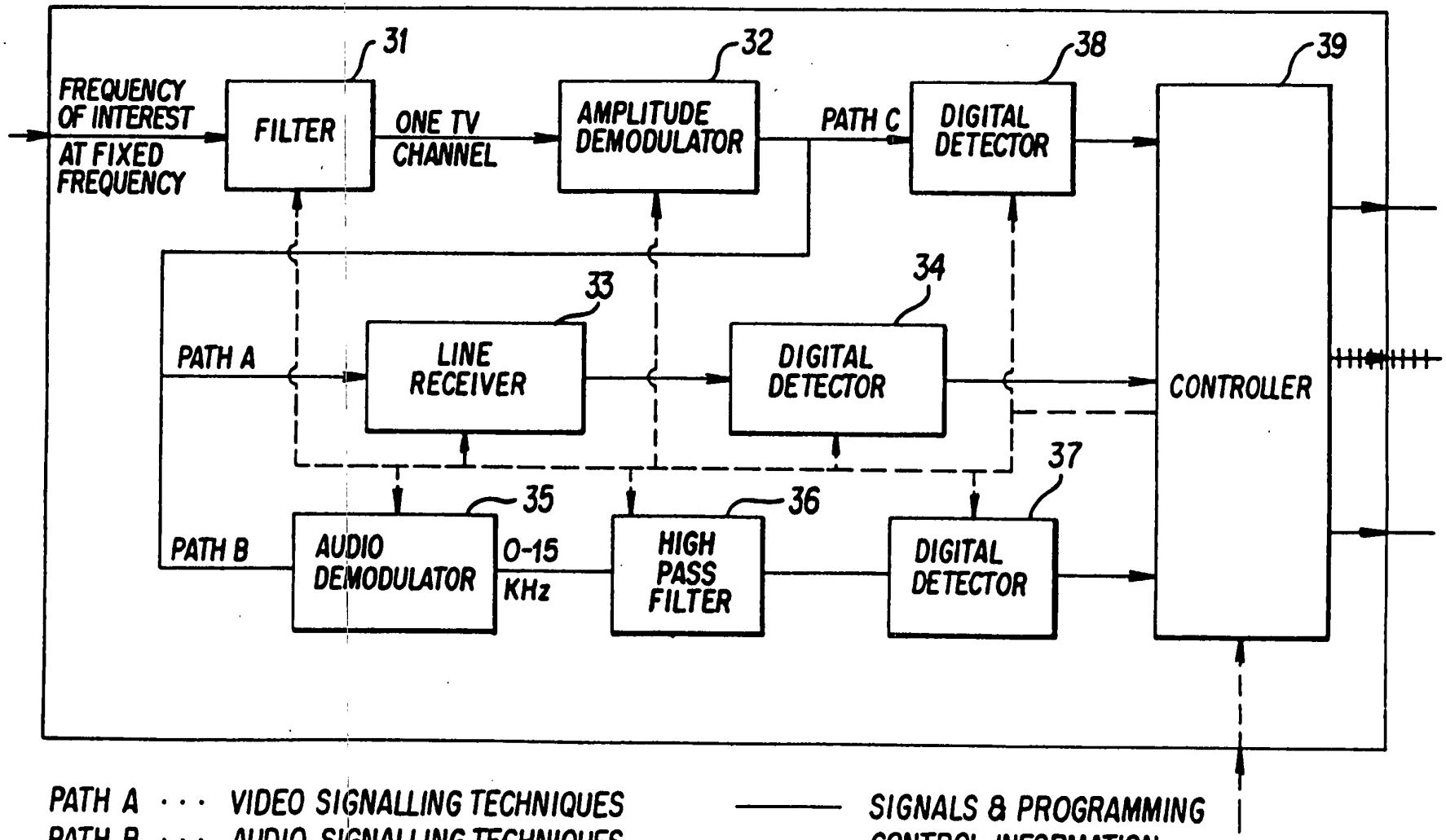


FIG. 2A

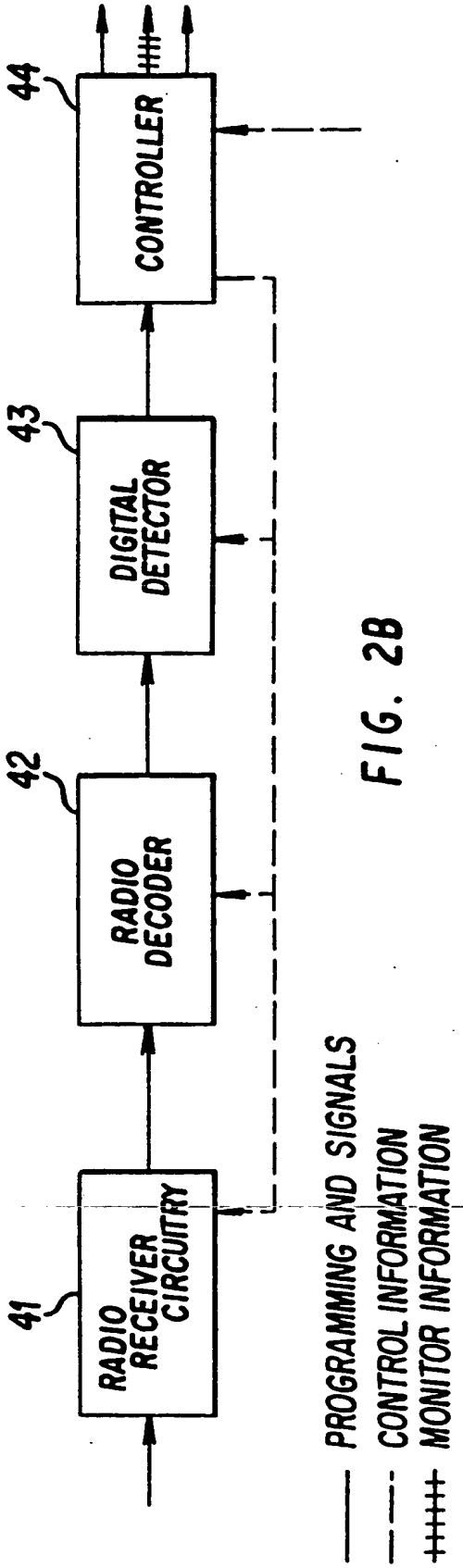

41
RADIO RECEIVER CIRCUITRY
42
RADIO DECODER
43
DIGITAL DETECTOR
44
CONTROLLER
PROGRAMMING AND SIGNALS
CONTROL INFORMATION
MONITOR INFORMATION

FIG. 2B

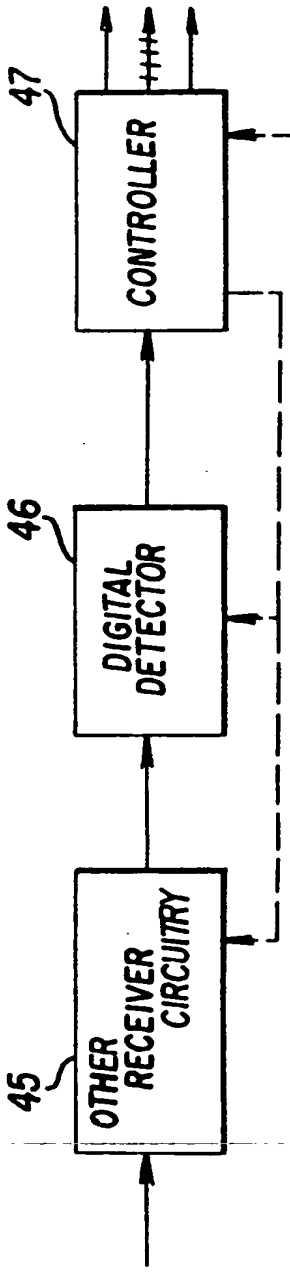

45
OTHER RECEIVER CIRCUITRY
46
DIGITAL DETECTOR
47
CONTROLLER

FIG. 2C

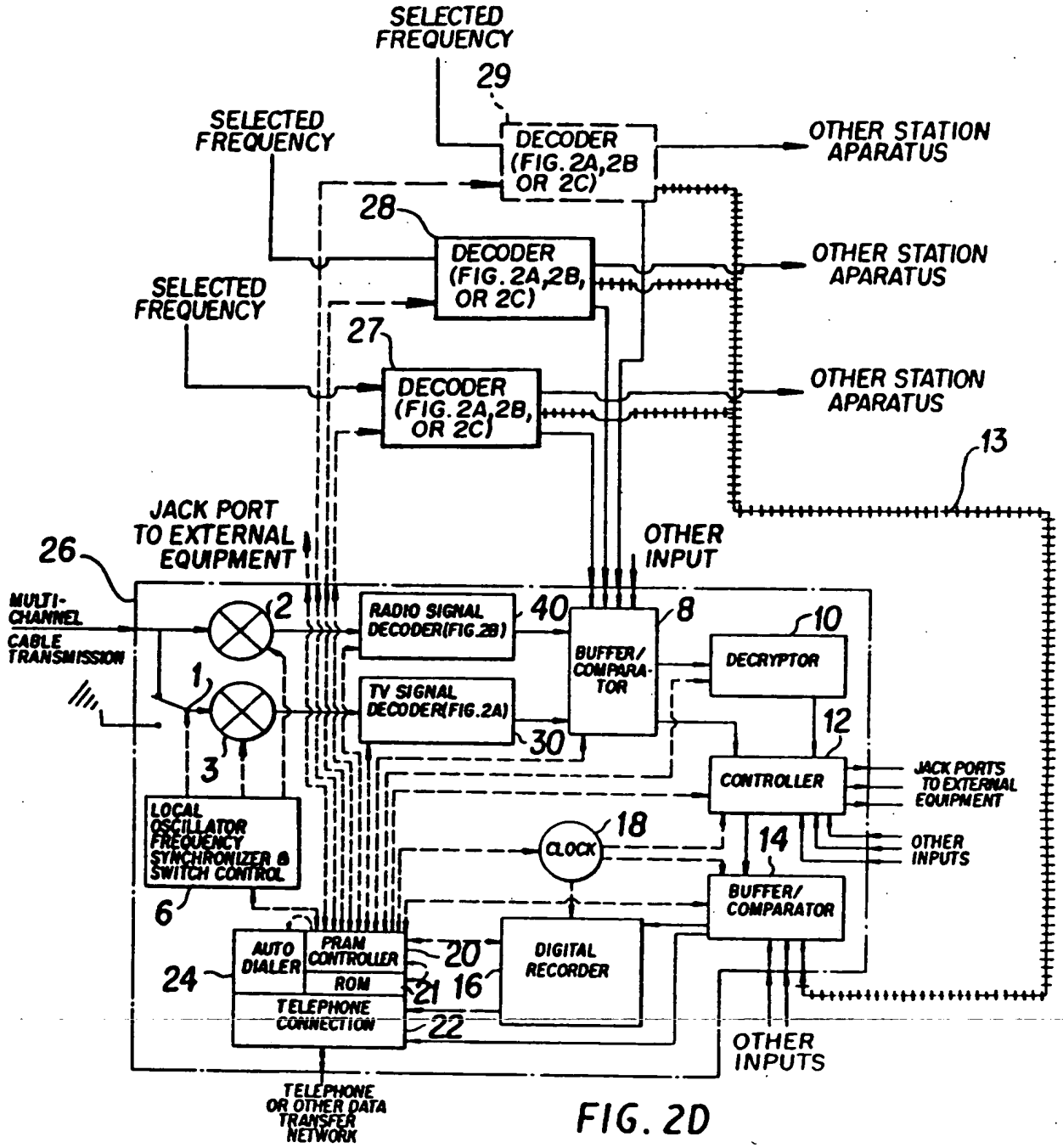


FIG. 2D

SIGNALS & PROGRAMMING
CONTROL INFORMATION
MONITOR INFORMATION

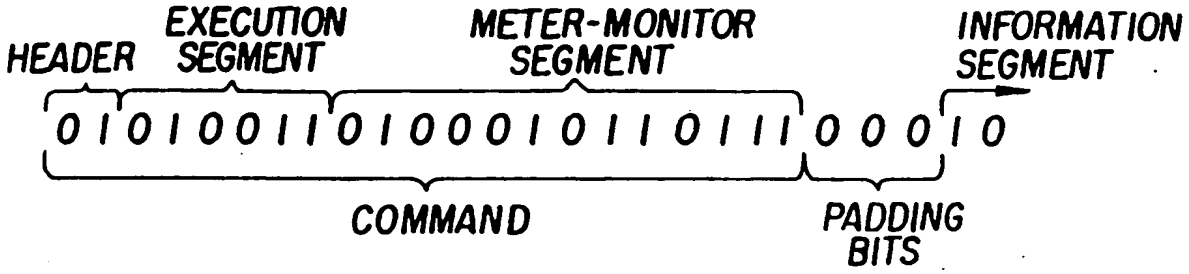


FIG. 2E

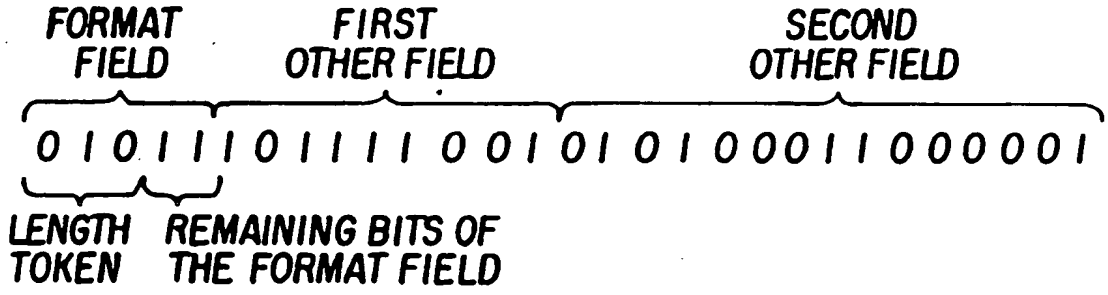


FIG. 2F

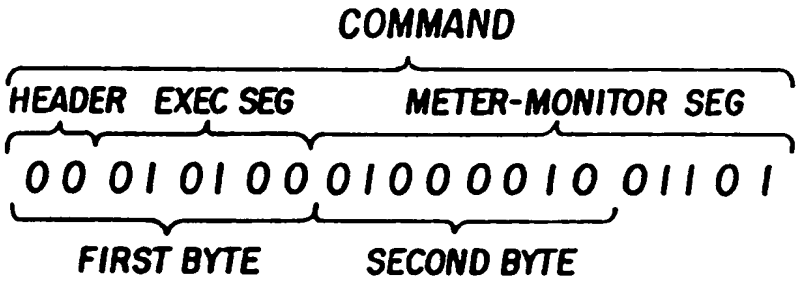


FIG. 2G

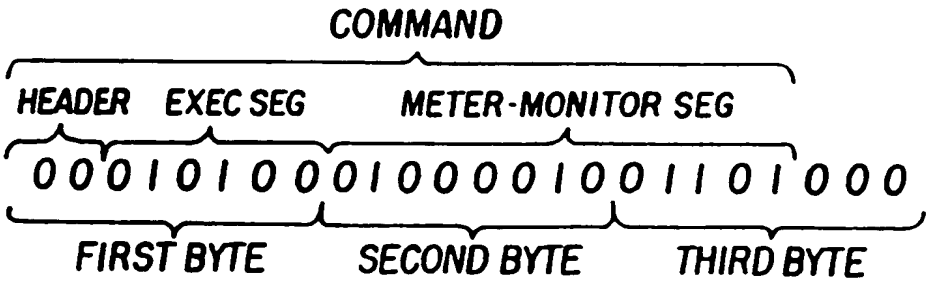


FIG. 2H

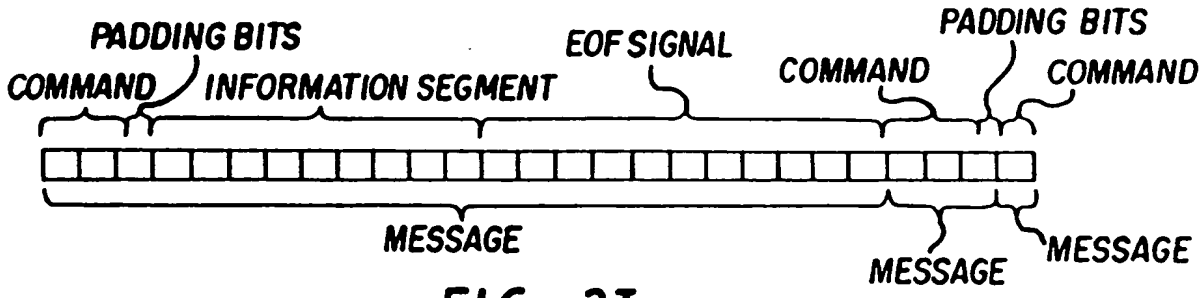


FIG. 2I

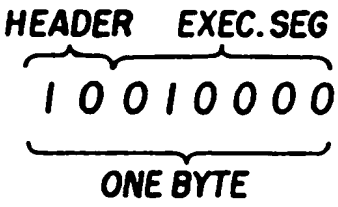


FIG. 2J

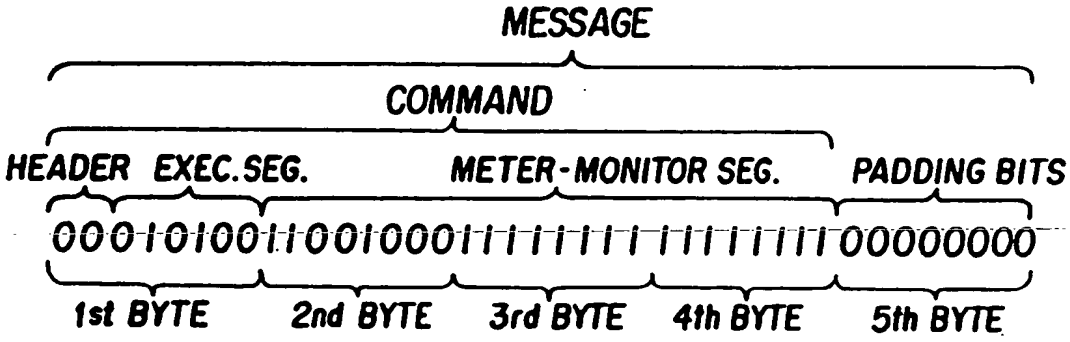


FIG. 2K

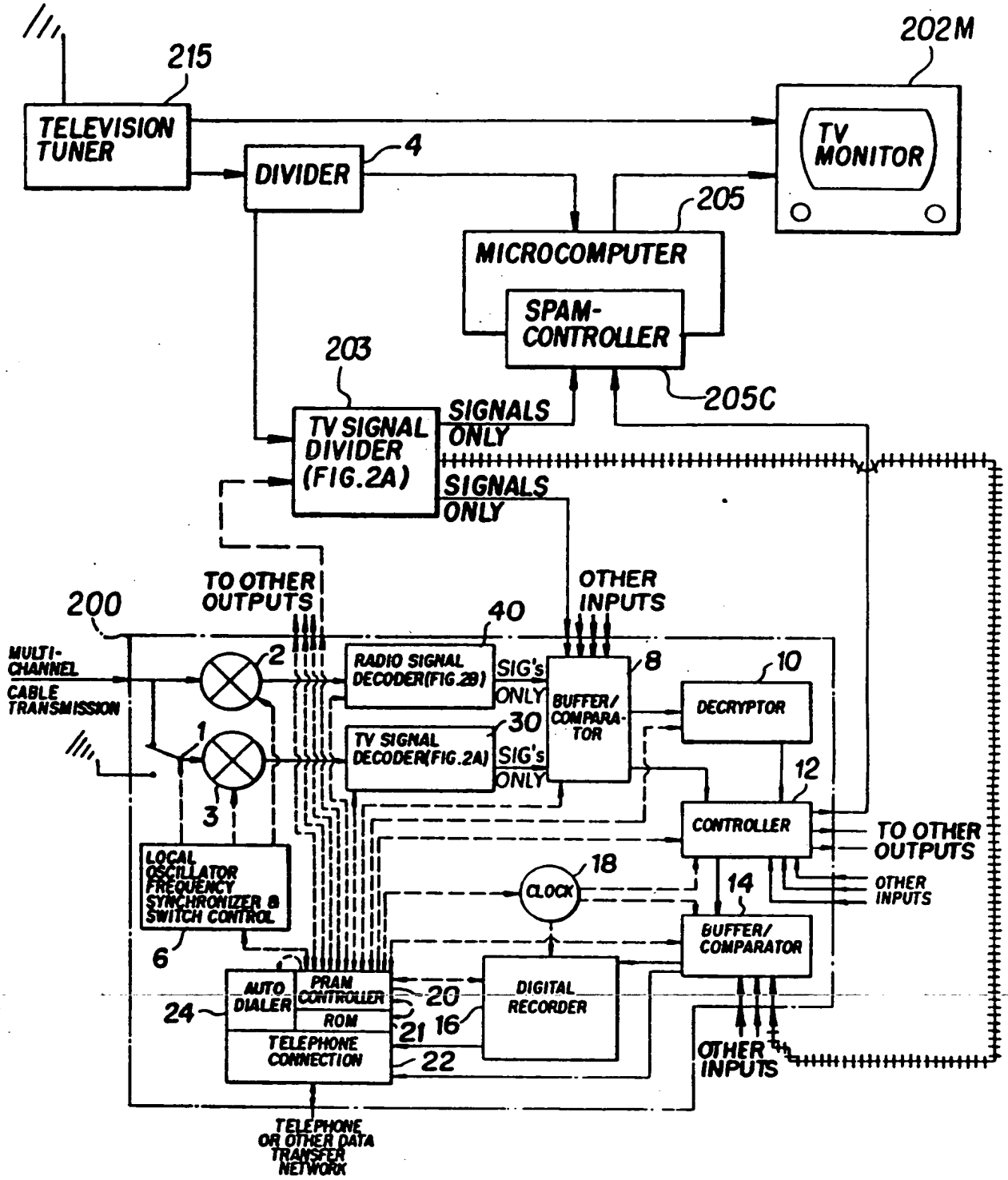

215
TELEVISION TUNER
DIVIDER
4
202M
TV MONITOR
205
MICROCOMPUTER
SPAM-CONTROLLER
205C
203
TV SIGNAL DIVIDER (FIG.2A)
SIGNALS ONLY
SIGNALS ONLY
TO OTHER OUTPUTS
OTHER INPUTS
200
40
MULTI-CHANNEL CABLE TRANSMISSION
2
1
3
RADIO SIGNAL DECODER(FIG.2B)
TV SIGNAL DECODER(FIG.2A)
SIG's ONLY
SIG's ONLY
30
BUFFER/ COMPARA-TOR
8
10
DECRYPTOR
12
CONTROLLER
TO OTHER OUTPUTS
OTHER INPUTS
LOCAL OSCILLATOR FREQUENCY SYNCHRONIZER & SWITCH CONTROL
6
18
CLOCK
14
BUFFER/ COMPARATOR
AUTO DIALER
PRAM CONTROLLER
ROM
TELEPHONE CONNECTION
24
20
21
16
22
DIGITAL RECORDER
OTHER INPUTS
TELEPHONE OR OTHER DATA TRANSFER NETWORK
SIGNALS & PROGRAMMING
CONTROL INFORMATION

FIG. 3

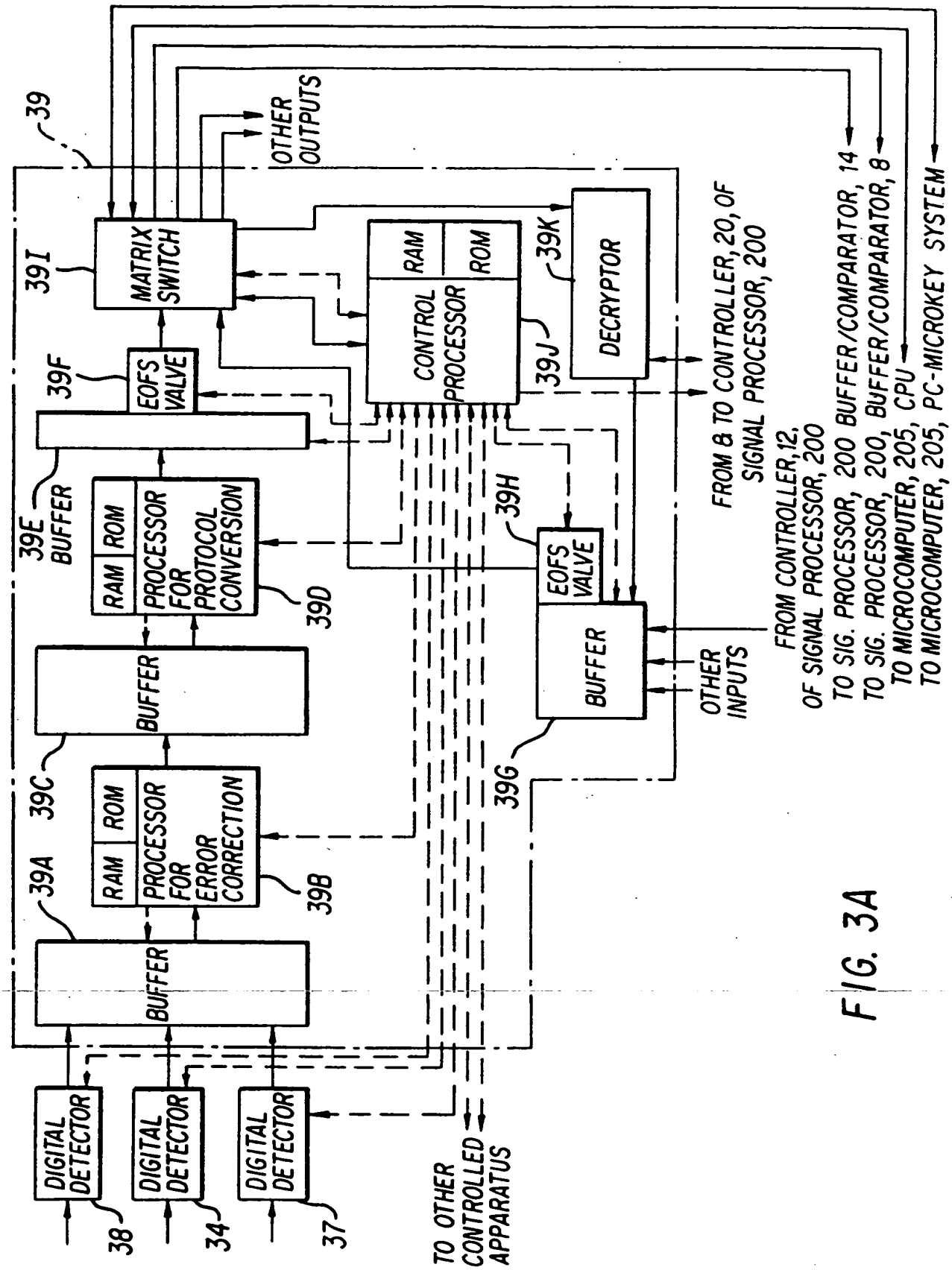
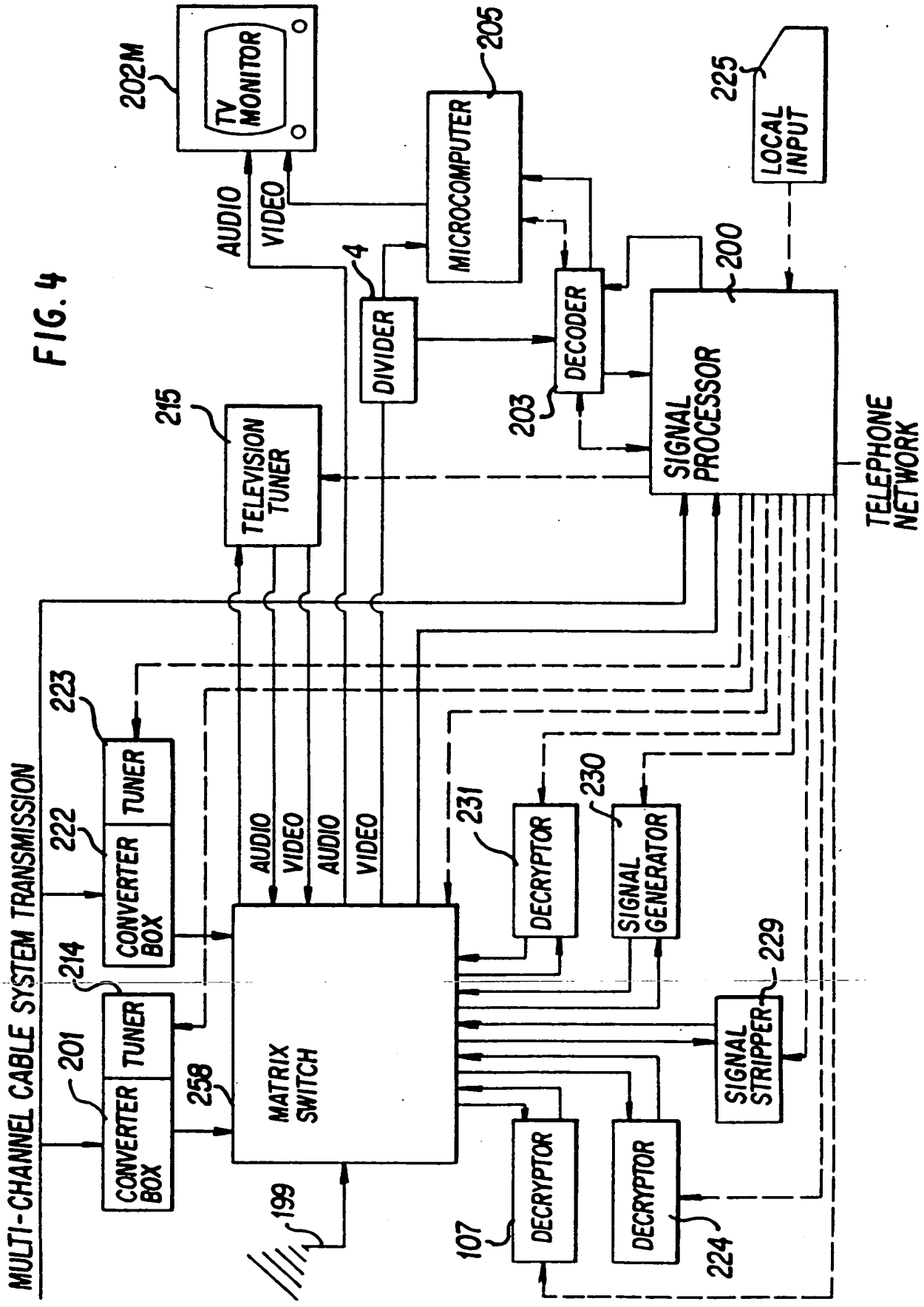

39
OTHER OUTPUTS
39I
MATRIX SWITCH
39F
EOFS VALVE
39E BUFFER
RAM
ROM
PROCESSOR FOR PROTOCOL CONVERSION
39D
BUFFER
39C
RAM
ROM
PROCESSOR FOR ERROR CORRECTION
39B
39A
BUFFER
DIGITAL DETECTOR
38
DIGITAL DETECTOR
34
DIGITAL DETECTOR
37
TO OTHER CONTROLLED APPARATUS
RAM
ROM
CONTROL PROCESSOR
39J
39K
DECRYPTOR
39H
EOFS VALVE
BUFFER
39G
OTHER INPUTS
FROM & TO CONTROLLER, 20, OF SIGNAL PROCESSOR, 200
FROM CONTROLLER, 12, OF SIGNAL PROCESSOR, 200
TO SIG. PROCESSOR, 200 BUFFER/COMPARATOR, 14
TO SIG. PROCESSOR, 200, BUFFER/COMPARATOR, 8
TO MICROCOMPUTER, 205, CPU
TO MICROCOMPUTER, 205, PC-MICROKEY SYSTEM

FIG. 3A

FIG. 4


MULTI-CHANNEL CABLE SYSTEM TRANSMISSION
CONVERTER BOX
TUNER
201
214
CONVERTER BOX
TUNER
222
223
258
MATRIX SWITCH
199
AUDIO
VIDEO
AUDIO
VIDEO
TELEVISION TUNER
215
DIVIDER
4
AUDIO
VIDEO
202M
TV MONITOR
MICROCOMPUTER
205
DECODER
203
SIGNAL PROCESSOR
200
LOCAL INPUT
225
TELEPHONE NETWORK
231
DECRYPTOR
230
SIGNAL GENERATOR
229
SIGNAL STRIPPER
107
DECRYPTOR
DECRYPTOR
224

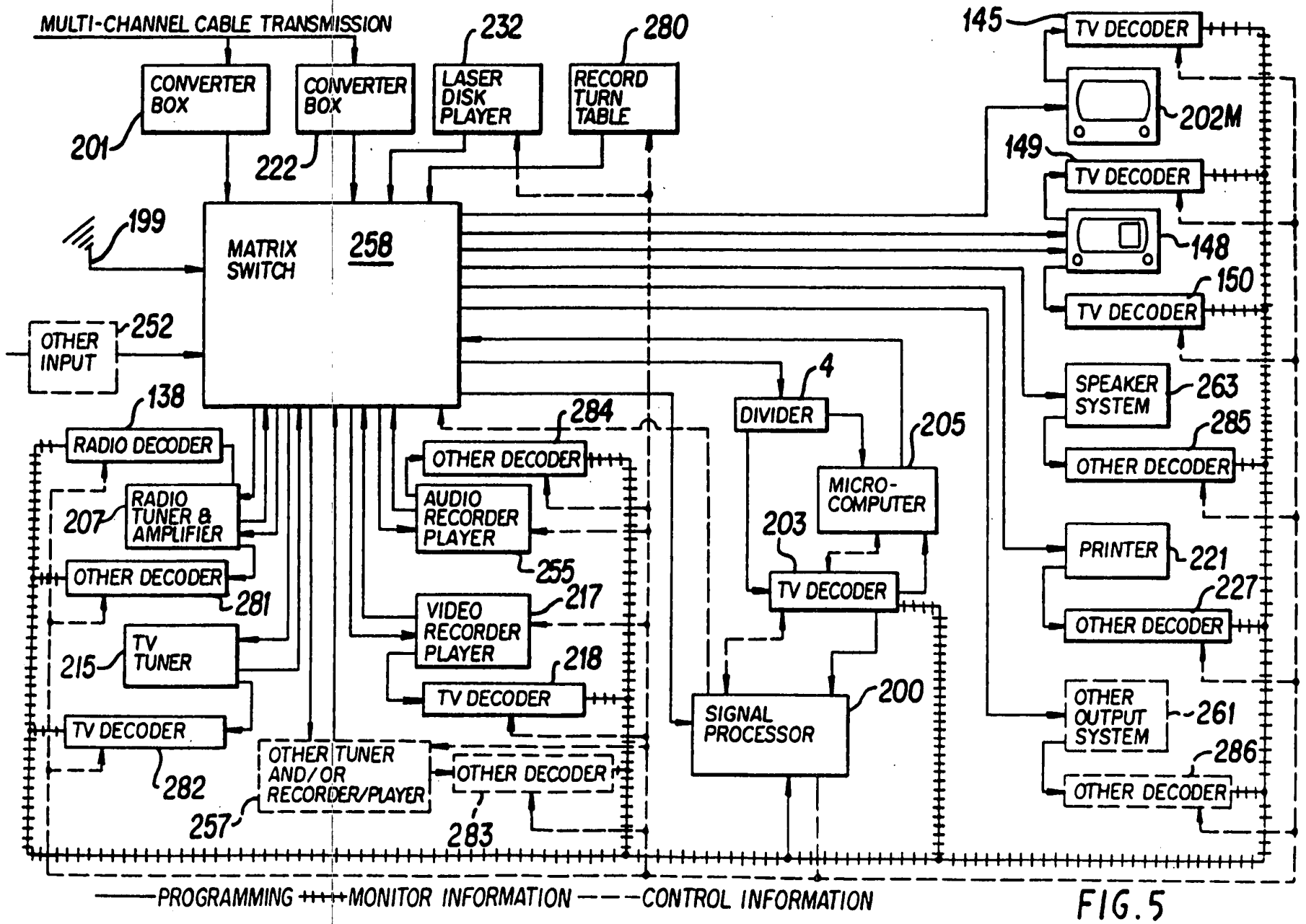


FIG. 5

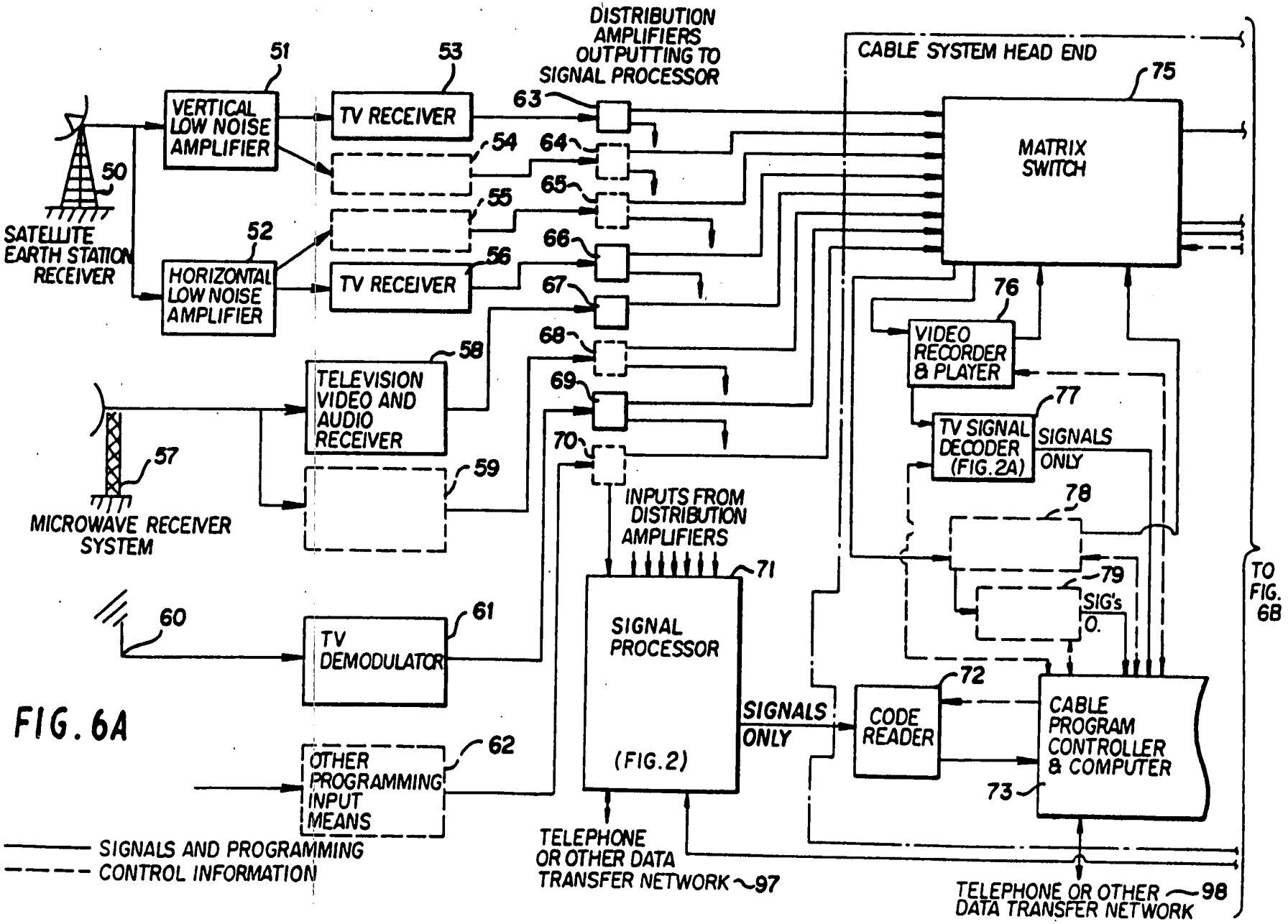


FIG. 6A

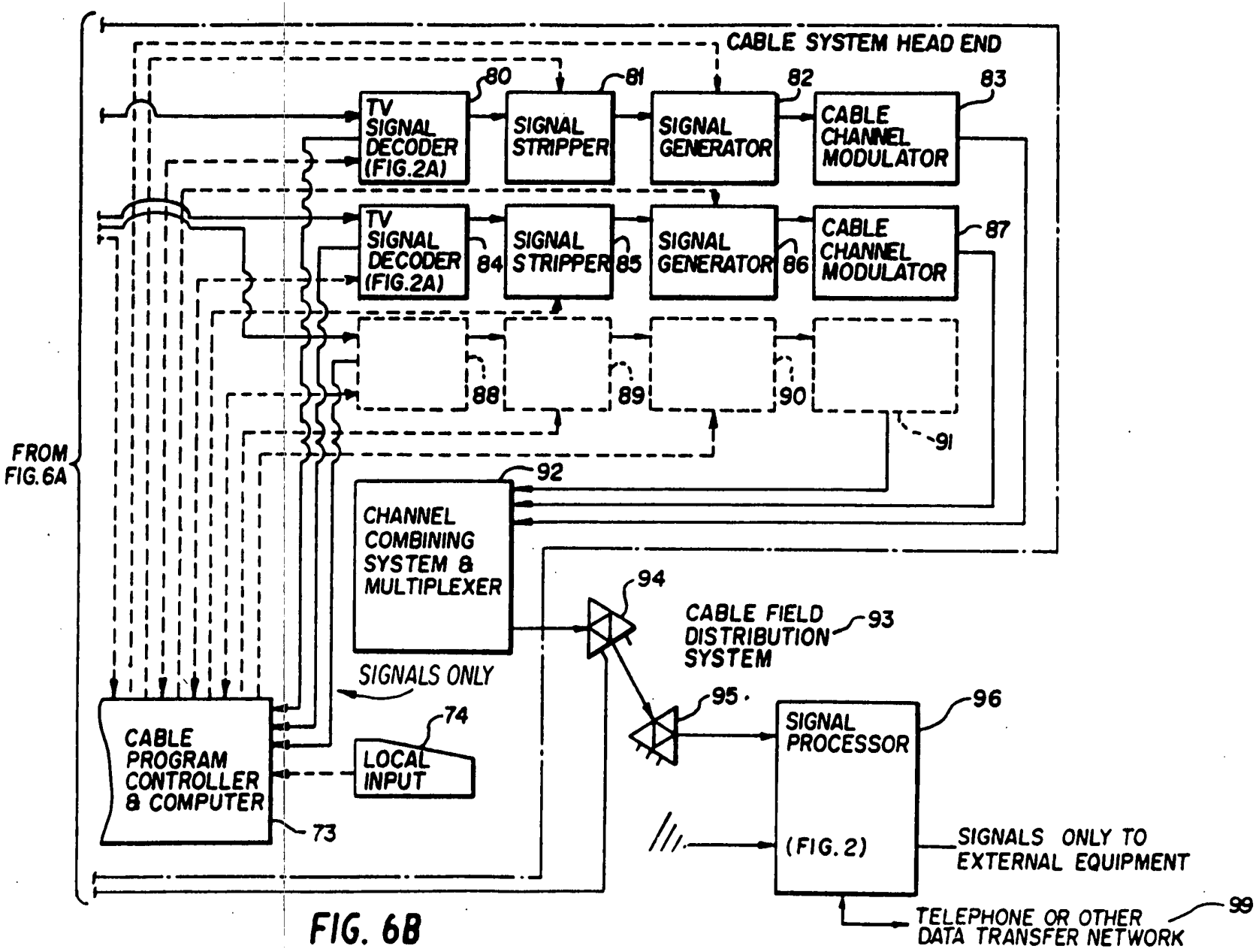


FIG. 6B

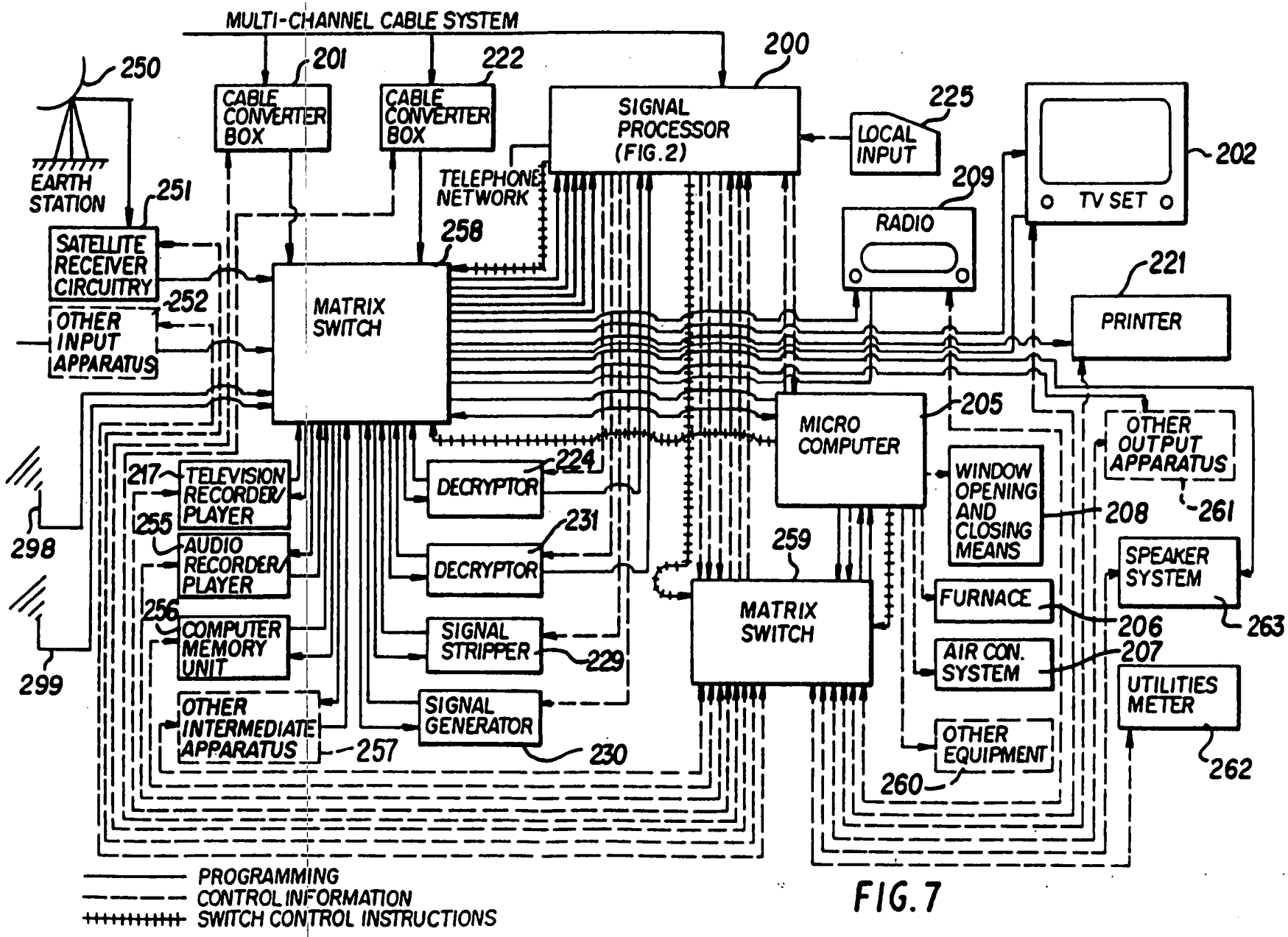


FIG.7

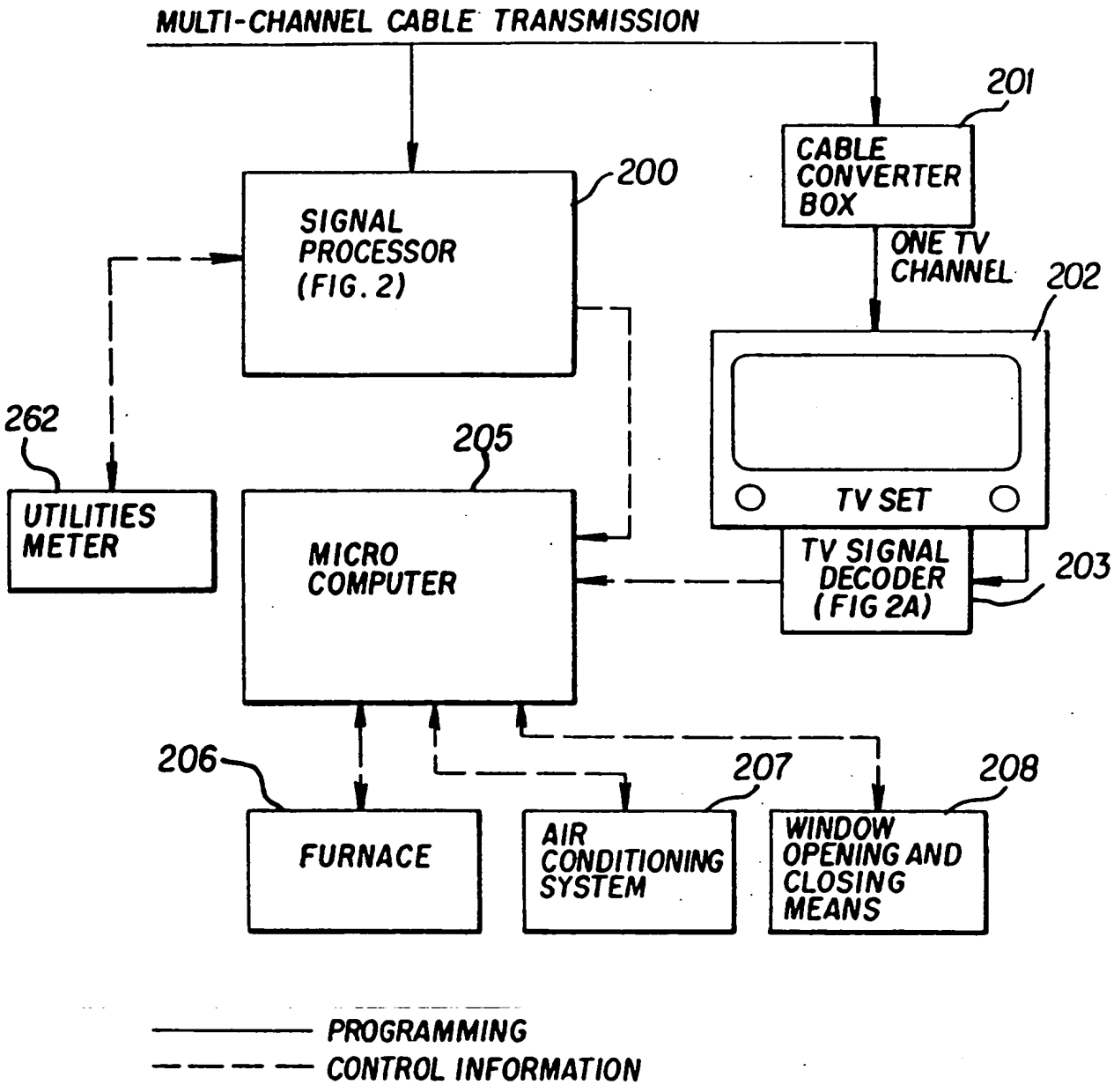

MULTI-CHANNEL CABLE TRANSMISSION
201
CABLE CONVERTER BOX
200
SIGNAL PROCESSOR (FIG. 2)
ONE TV CHANNEL
202
TV SET
262
205
UTILITIES METER
MICRO COMPUTER
TV SIGNAL DECODER (FIG 2A)
203
206
207
208
FURNACE
AIR CONDITIONING SYSTEM
WINDOW OPENING AND CLOSING MEANS
PROGRAMMING
CONTROL INFORMATION

FIG. 7A

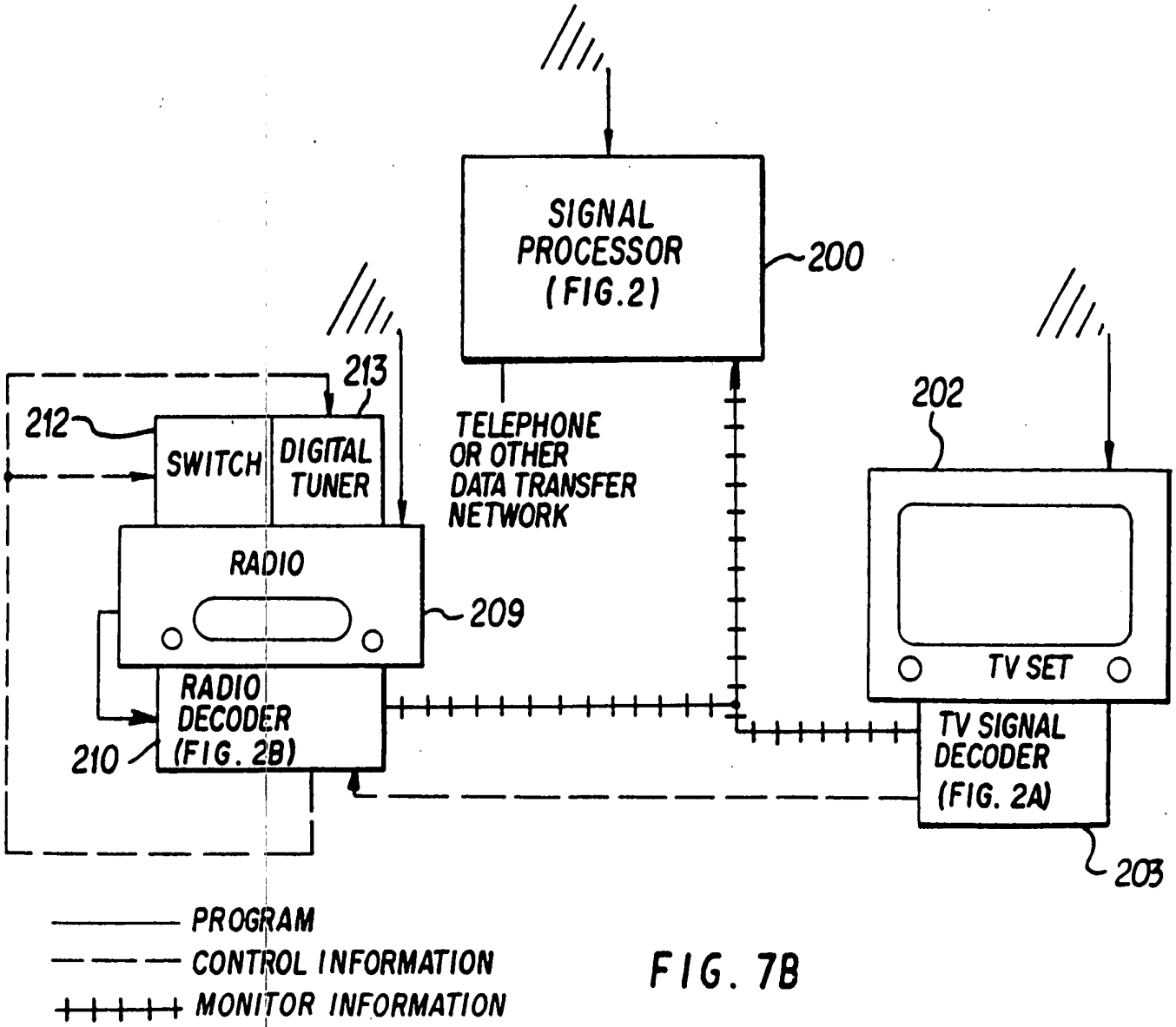


FIG. 7B

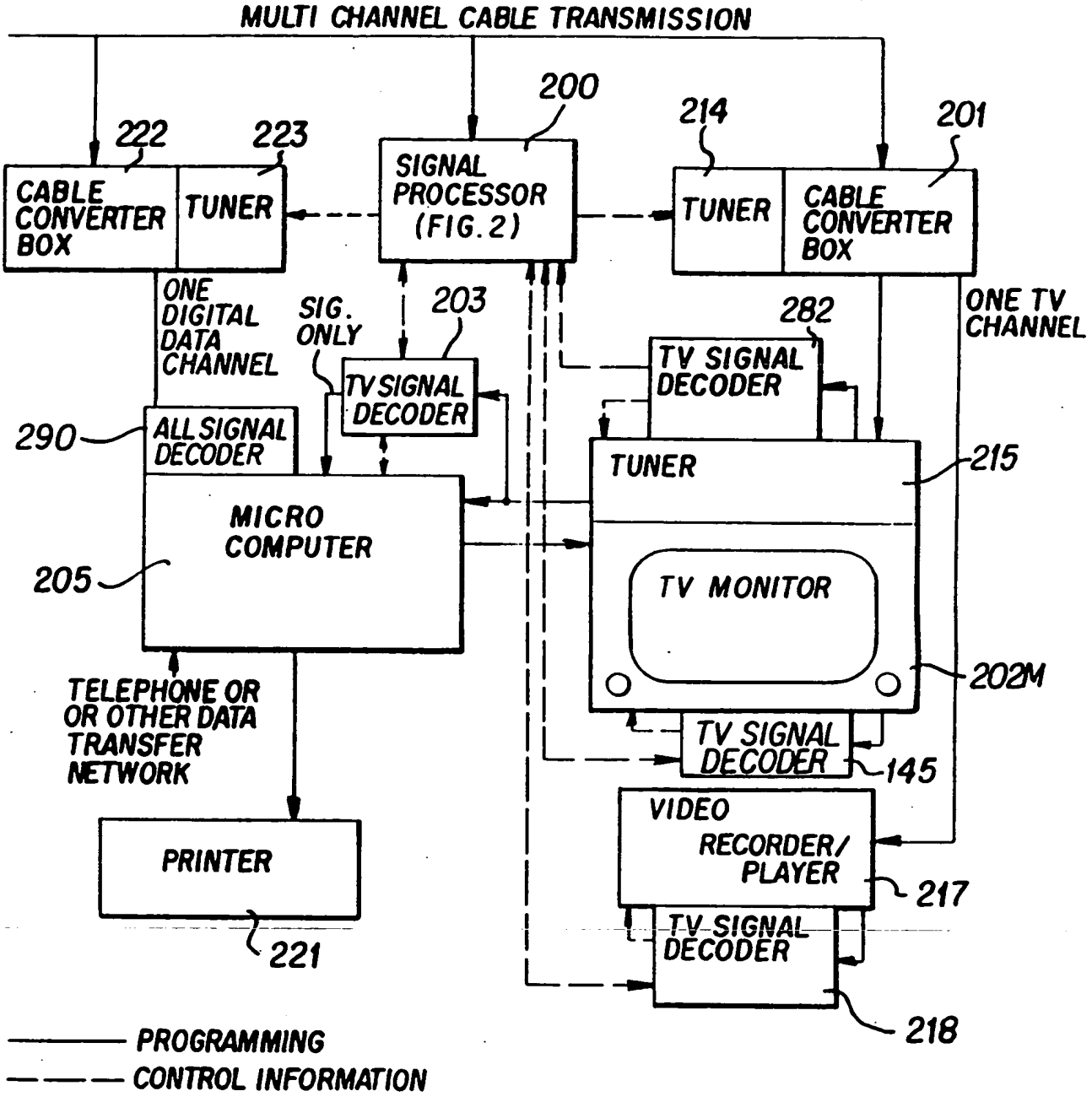

MULTI CHANNEL CABLE TRANSMISSION
200
214
201
222
223
CABLE CONVERTER BOX
TUNER
SIGNAL PROCESSOR (FIG. 2)
TUNER
CABLE CONVERTER BOX
ONE DIGITAL DATA CHANNEL
SIG. ONLY
203
282
ONE TV CHANNEL
TV SIGNAL DECODER
TV SIGNAL DECODER
290
ALL SIGNAL DECODER
TUNER
215
MICRO COMPUTER
205
TV MONITOR
202M
TELEPHONE OR OR OTHER DATA TRANSFER NETWORK
TV SIGNAL DECODER
145
VIDEO RECORDER/ PLAYER
217
PRINTER
221
TV SIGNAL DECODER
218
PROGRAMMING
CONTROL INFORMATION

FIG. 7C

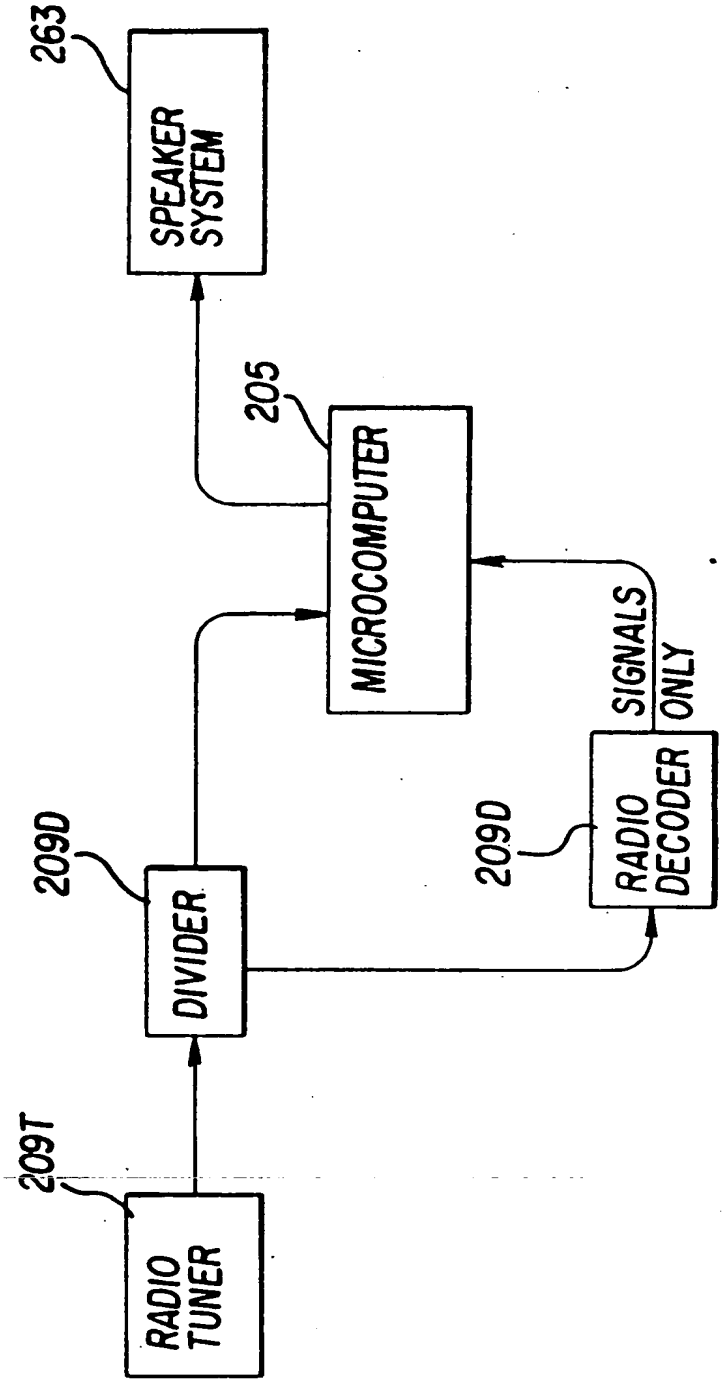

263
SPEAKER SYSTEM
205
MICROCOMPUTER
SIGNALS ONLY
209D
RADIO DECODER
209D
DIVIDER
209T
RADIO TUNER

FIG. 7D

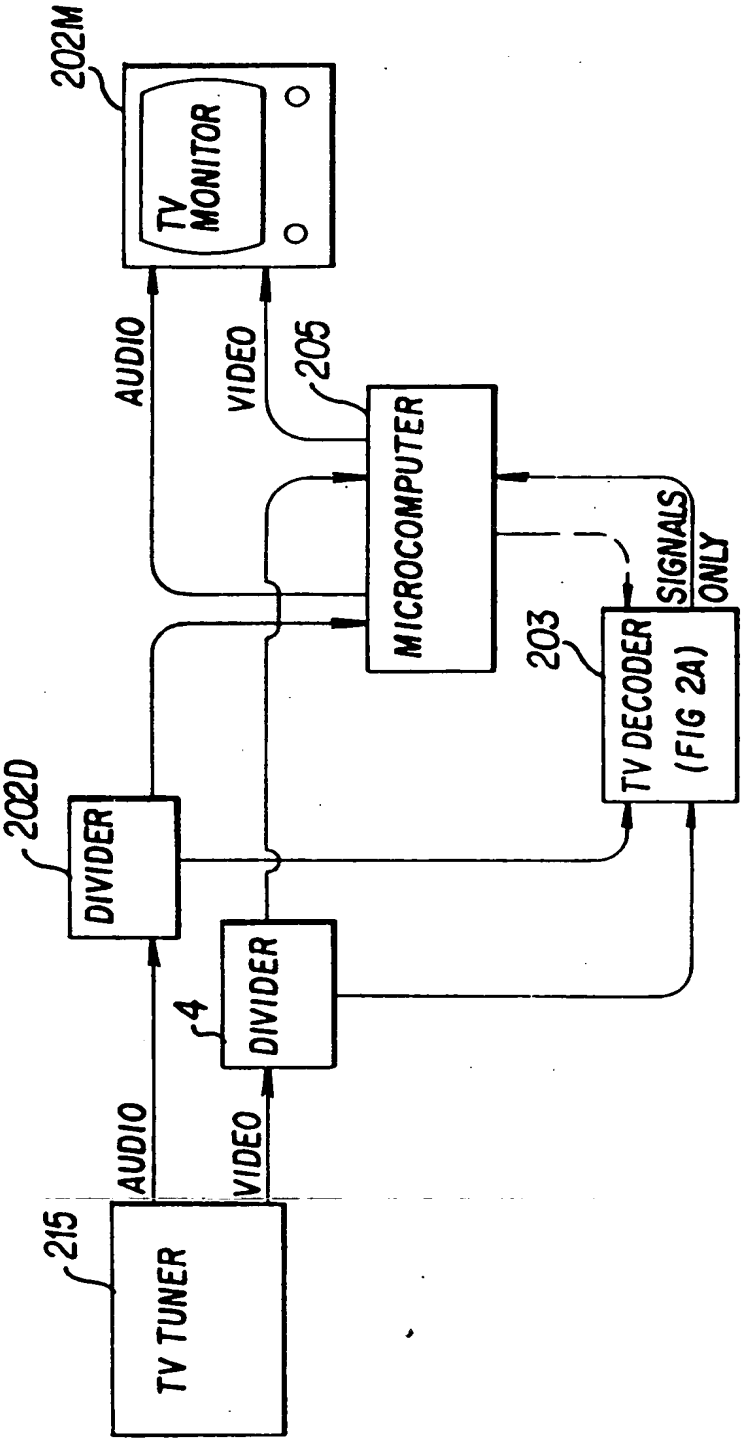

202M
TV MONITOR
AUDIO
VIDEO
205
MICROCOMPUTER
203
TV DECODER (FIG 2A)
SIGNALS ONLY
202D
DIVIDER
4
DIVIDER
AUDIO
VIDEO
215
TV TUNER

FIG. 7E

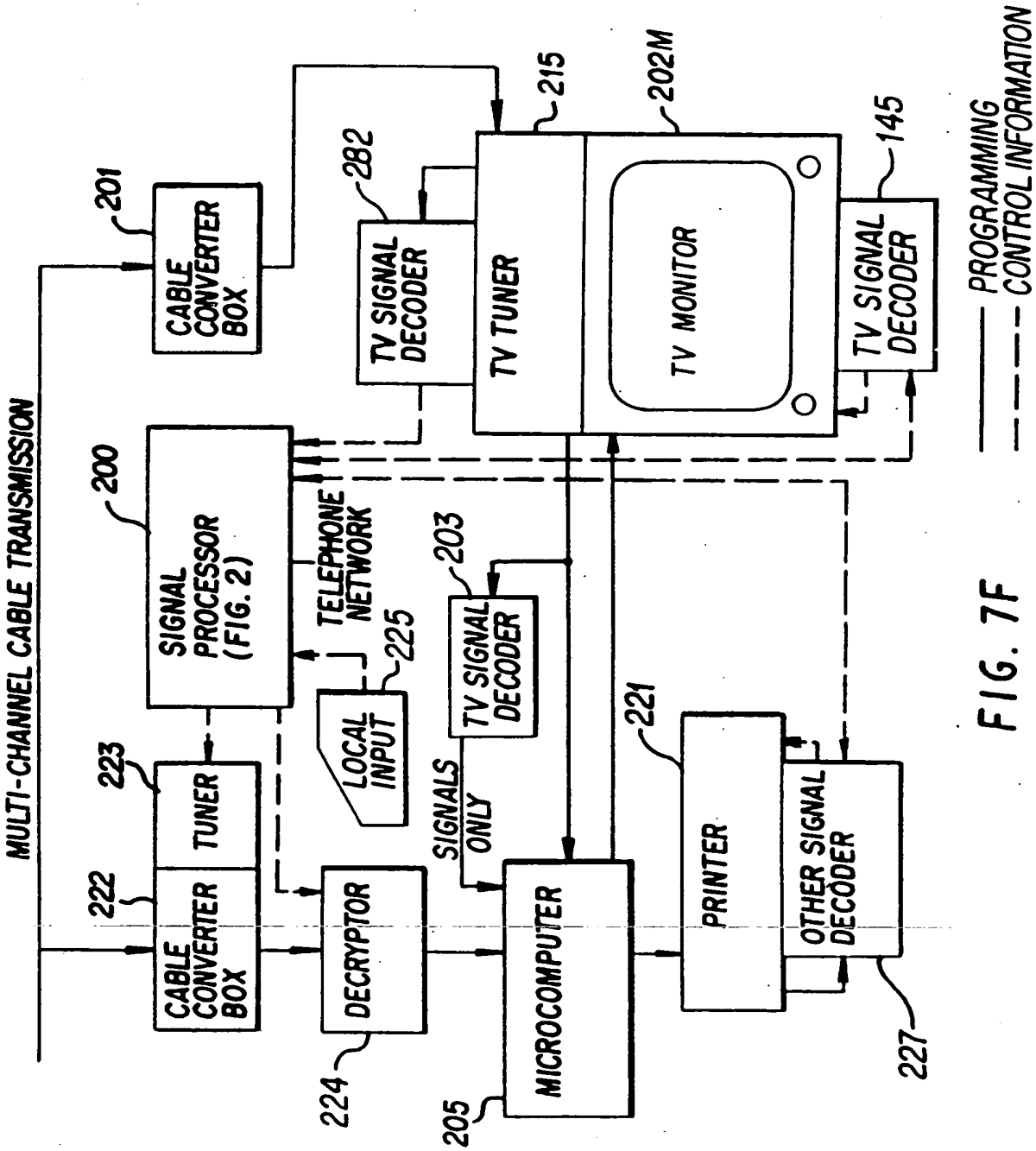

MULTI-CHANNEL CABLE TRANSMISSION
201
CABLE CONVERTER BOX
282
TV SIGNAL DECODER
215
TV TUNER
202M
TV MONITOR
145
TV SIGNAL DECODER
200
SIGNAL PROCESSOR (FIG. 2)
TELEPHONE NETWORK
225
LOCAL INPUT
203
TV SIGNAL DECODER
223
TUNER
222
CABLE CONVERTER BOX
DECRYPTOR
224
SIGNALS ONLY
205
MICROCOMPUTER
221
PRINTER
OTHER SIGNAL DECODER
227
PROGRAMMING
CONTROL INFORMATION

FIG. 7F

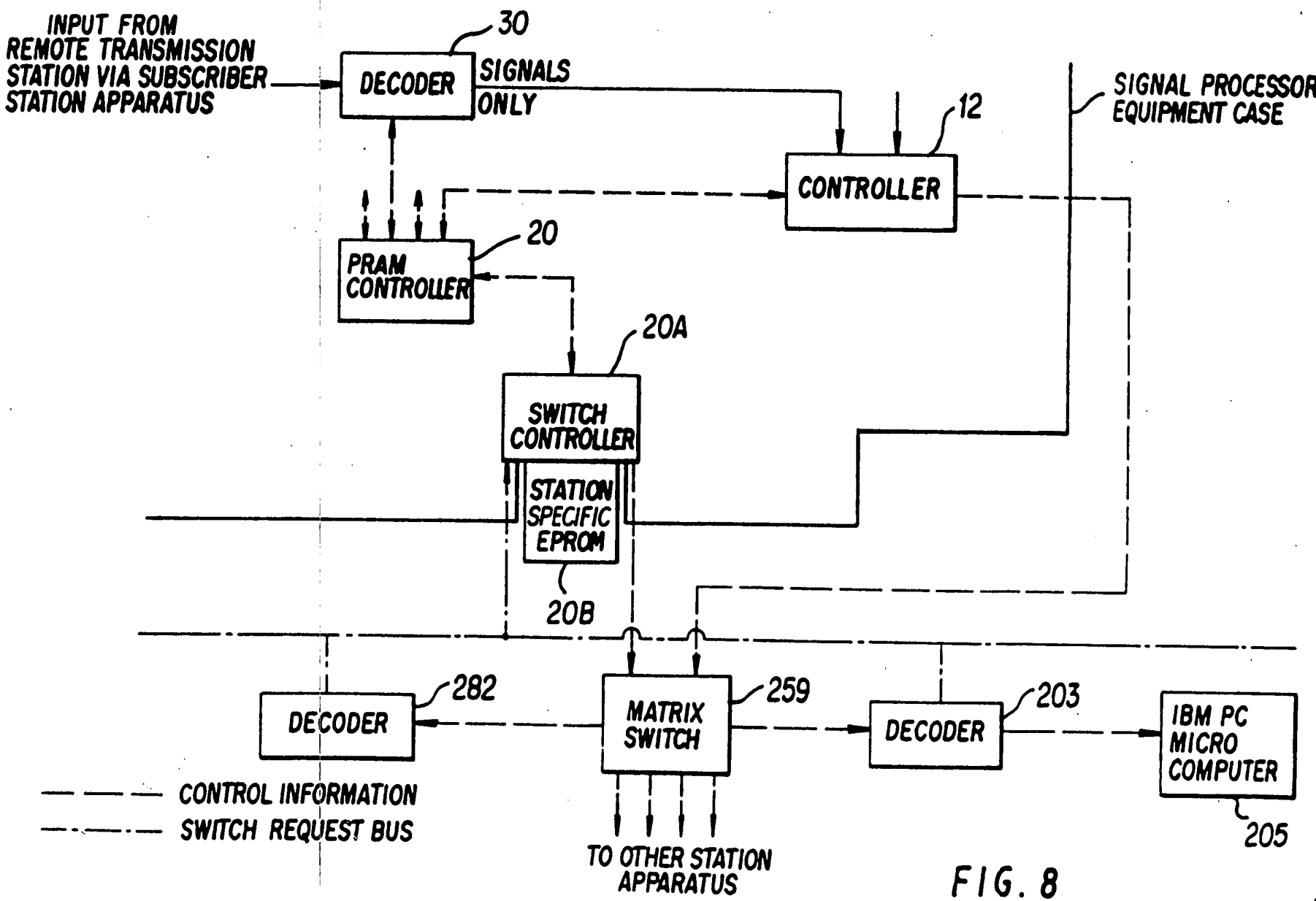


FIG. 8